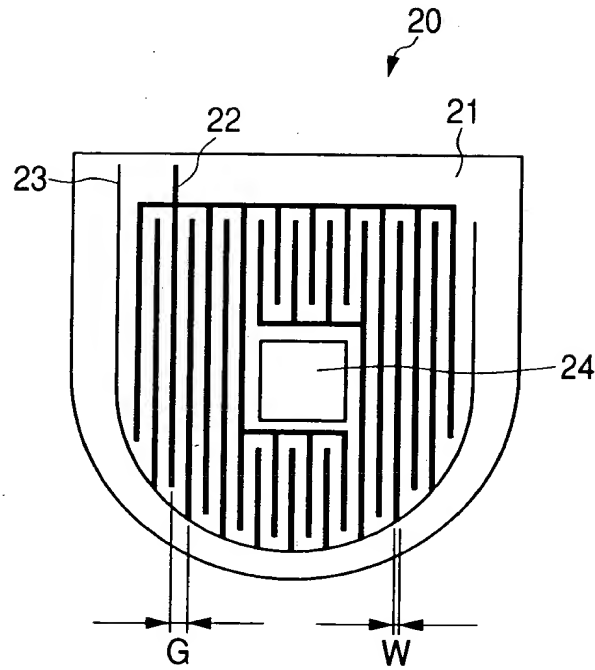


FIG. 3



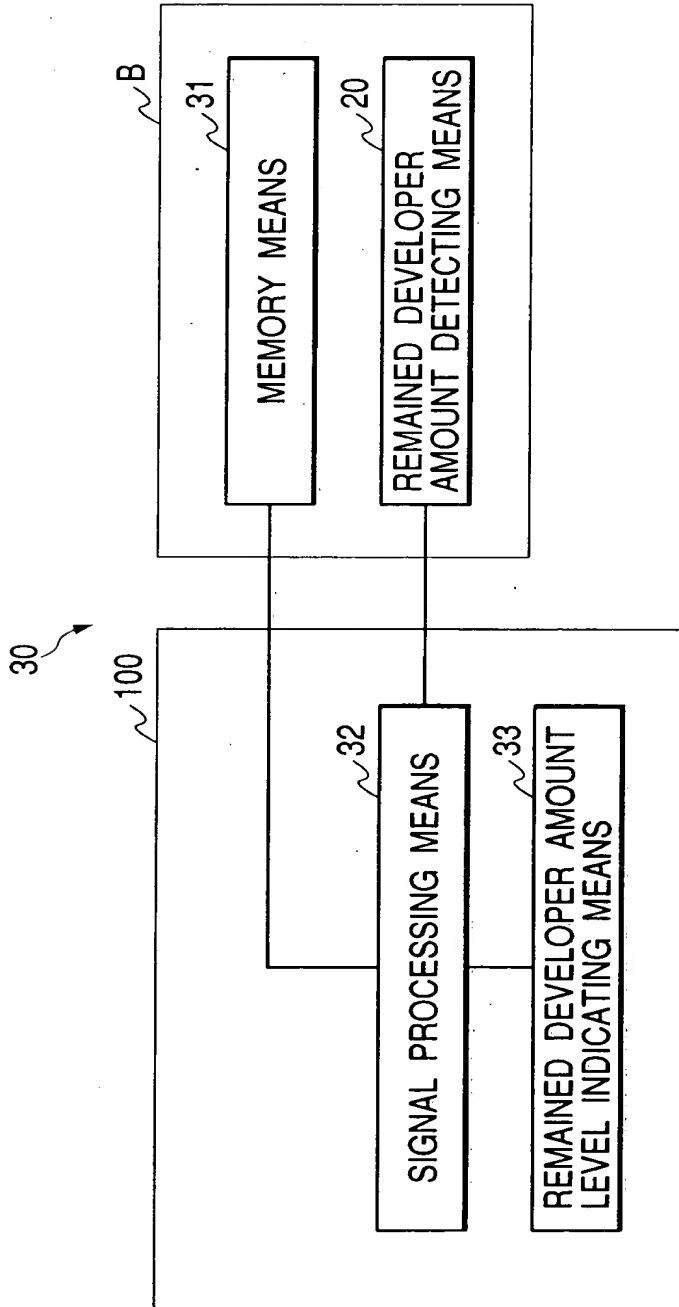
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APPROVED	O.G. FIG.	
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FIG. 4



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FIG. 5

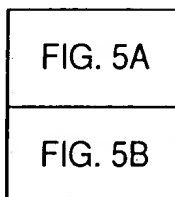
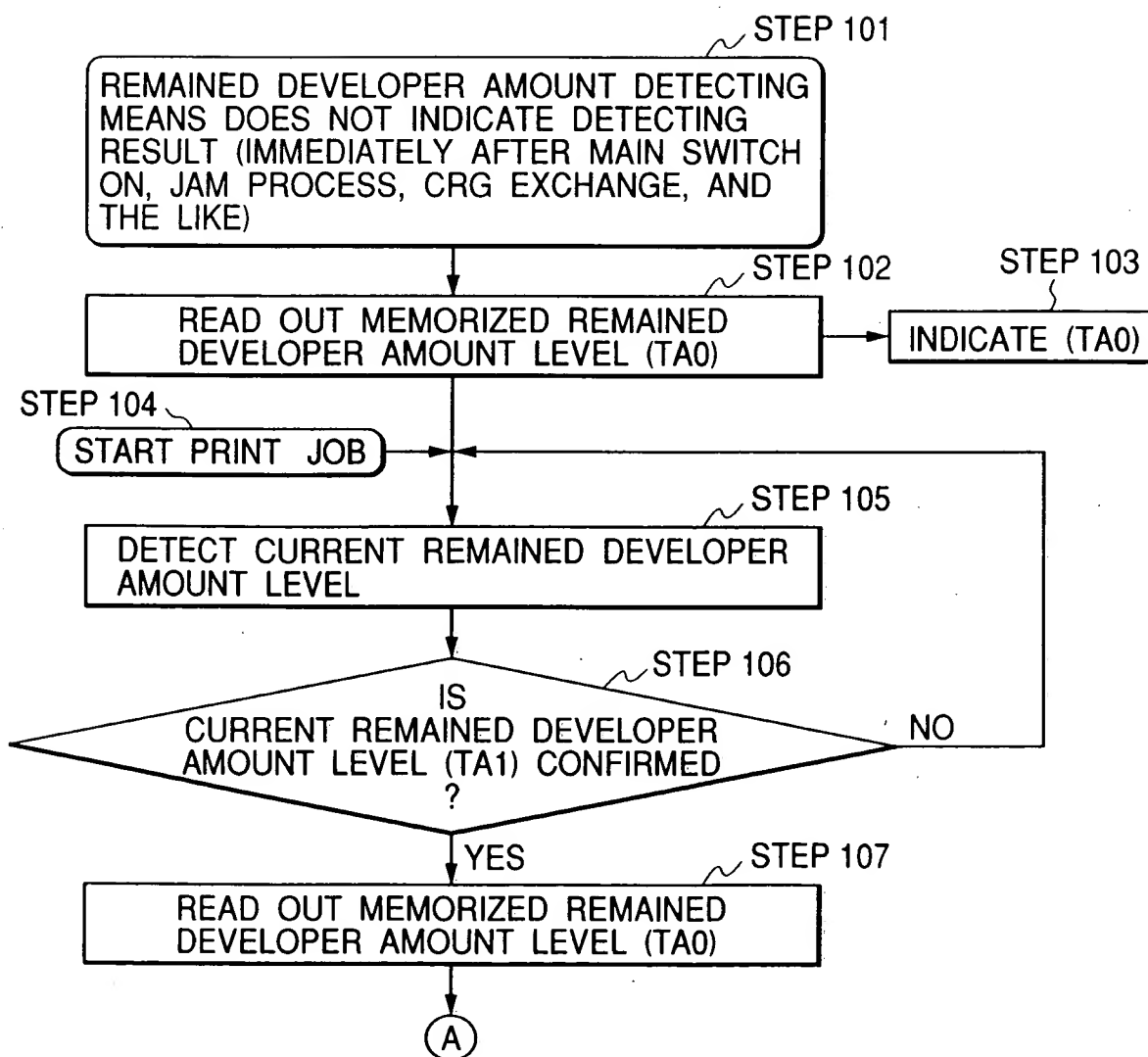
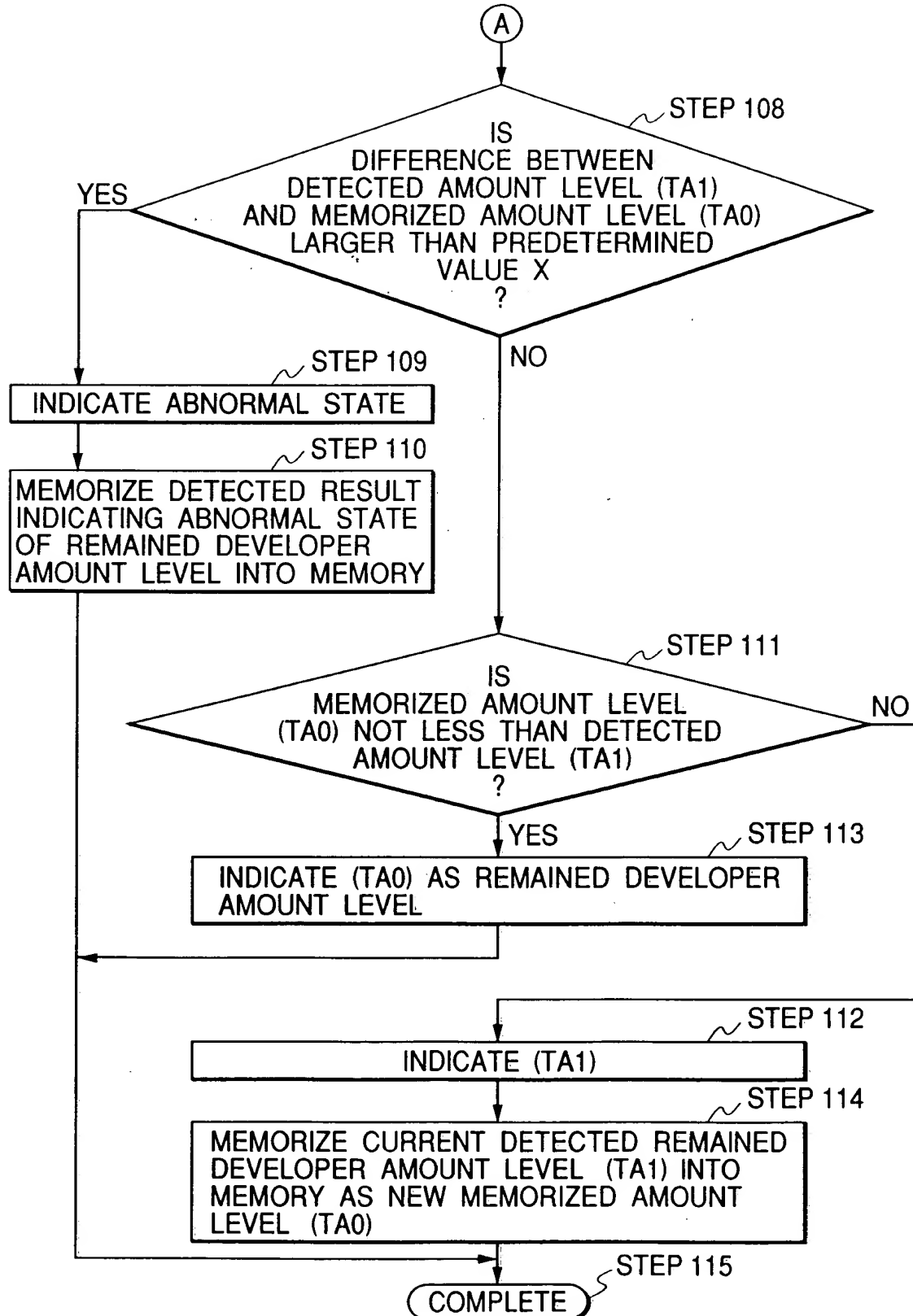


FIG. 5A



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FIG. 5B

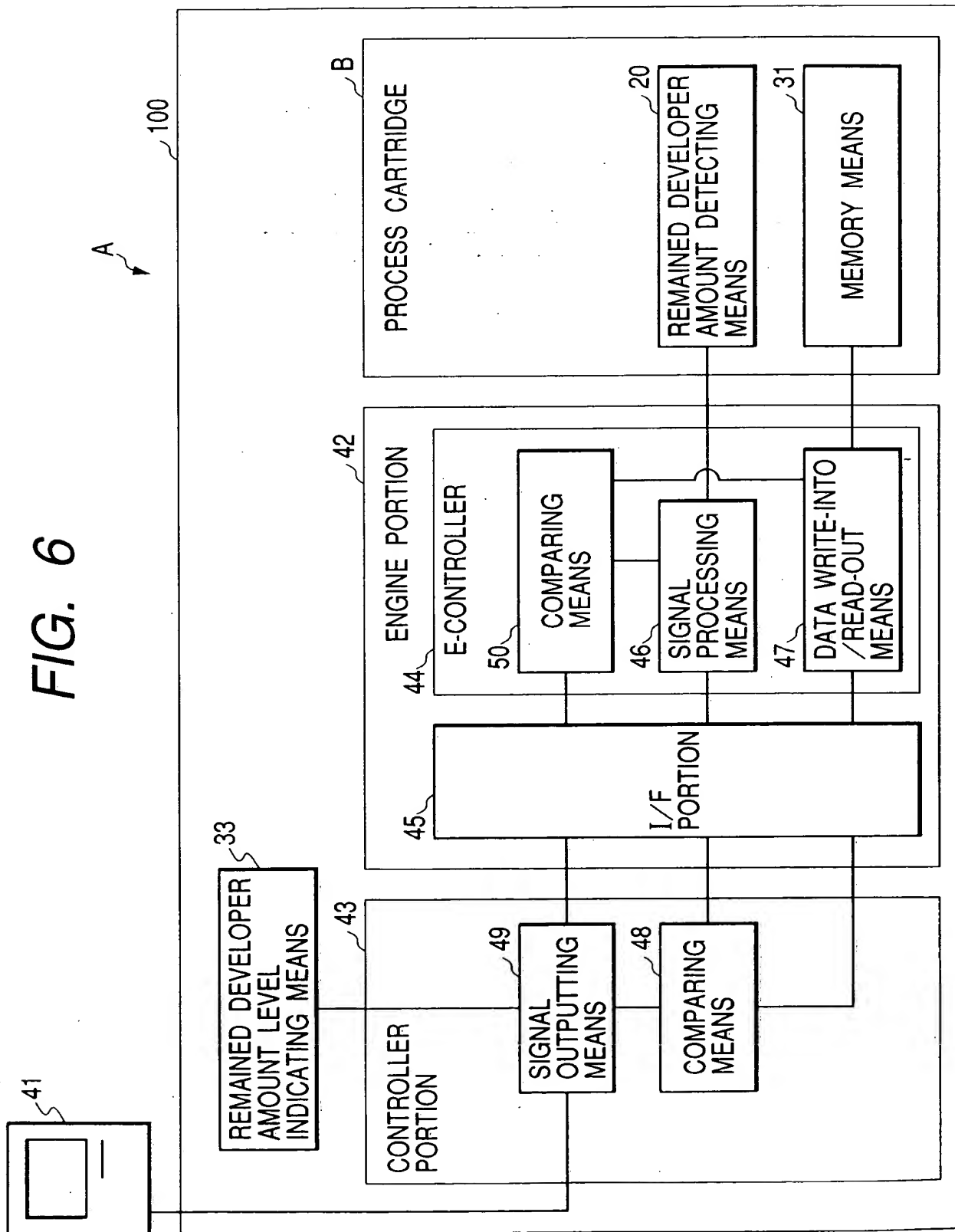


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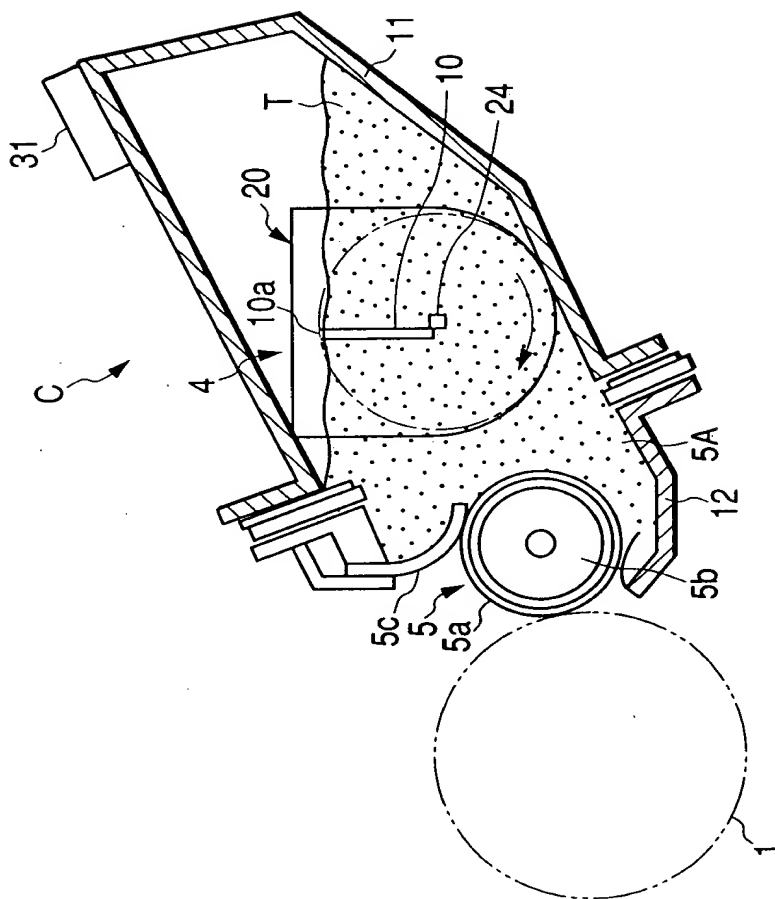
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FIG. 6



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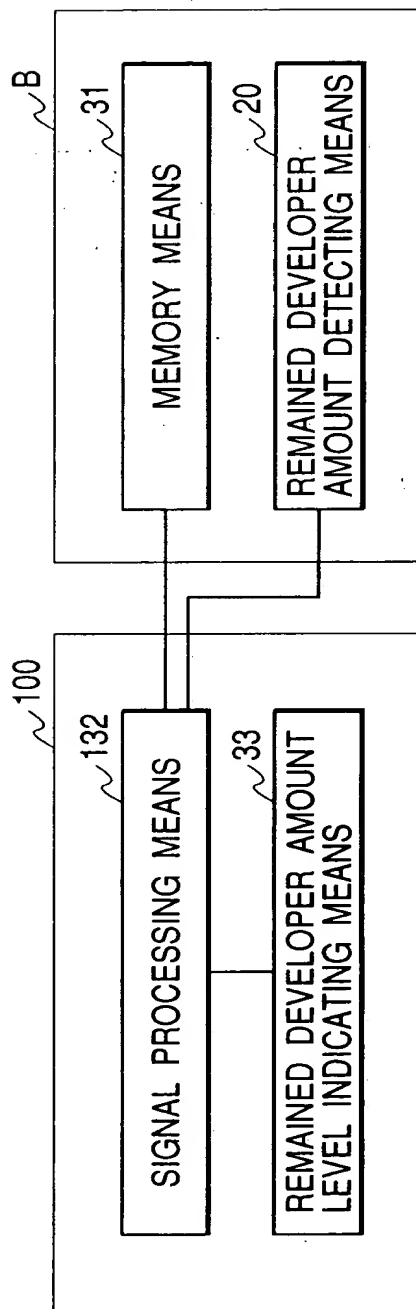
FIG. 7



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FIG. 8



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FIG. 9

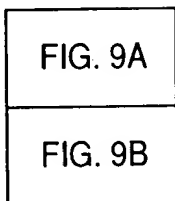


FIG. 9A

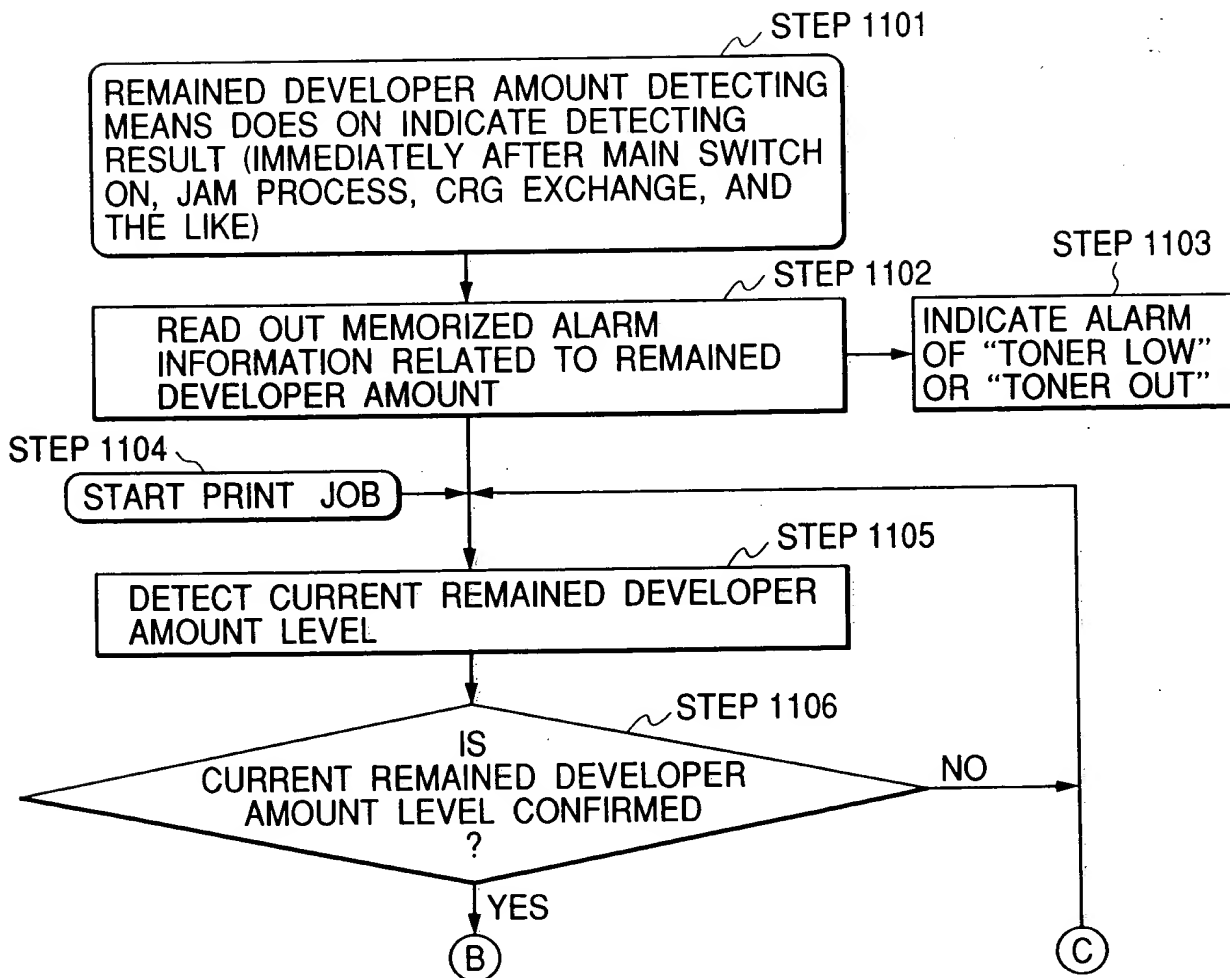
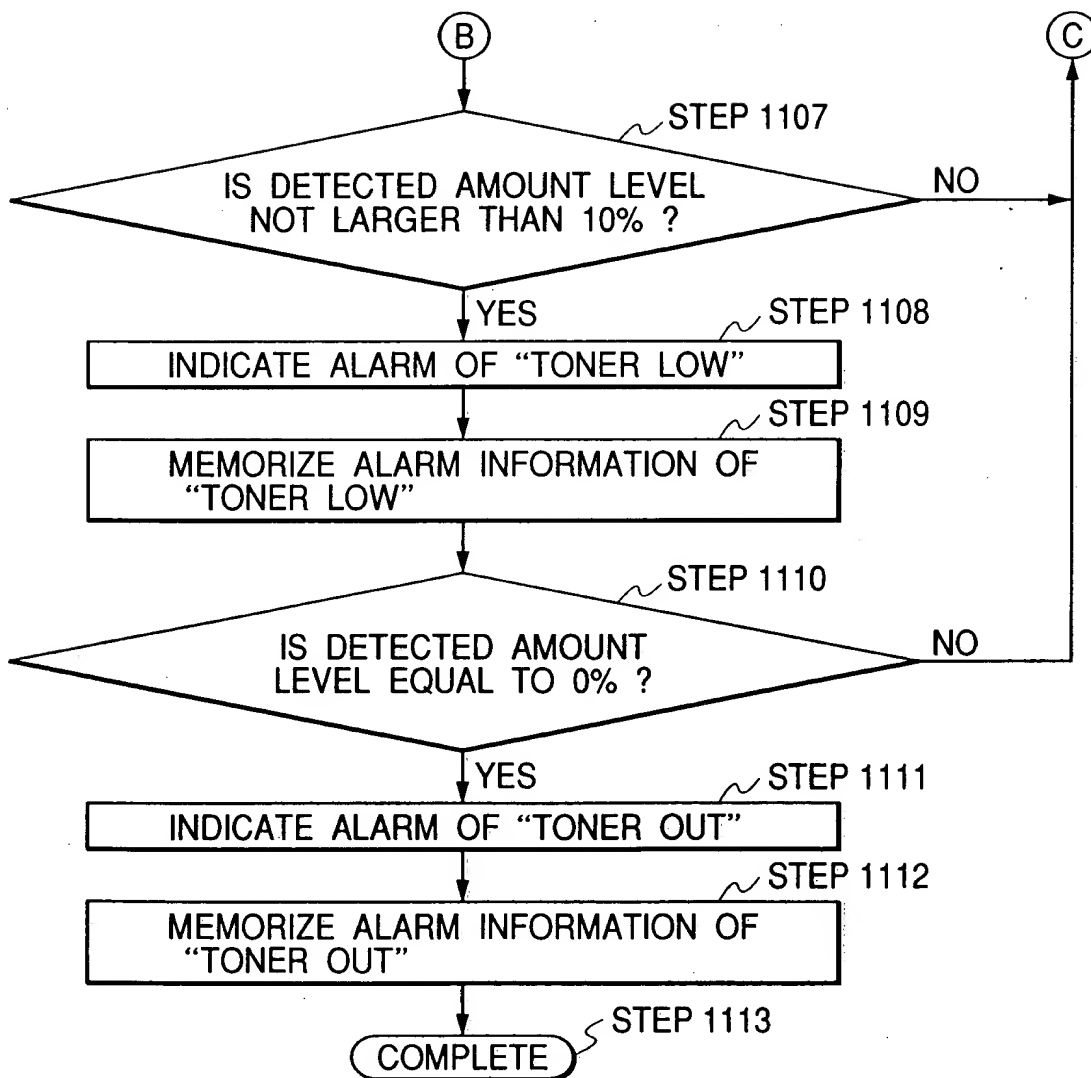
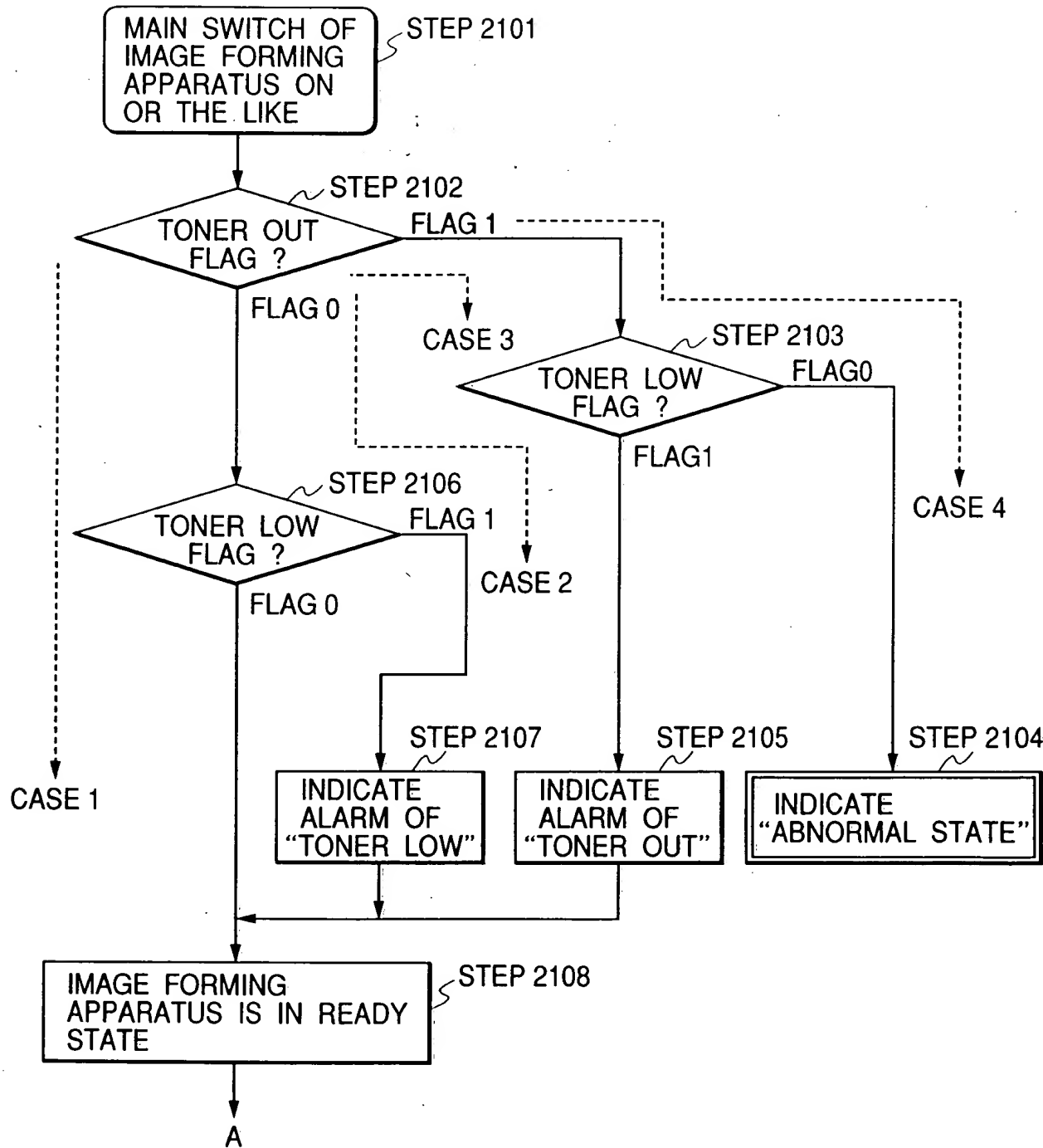


FIG. 9B



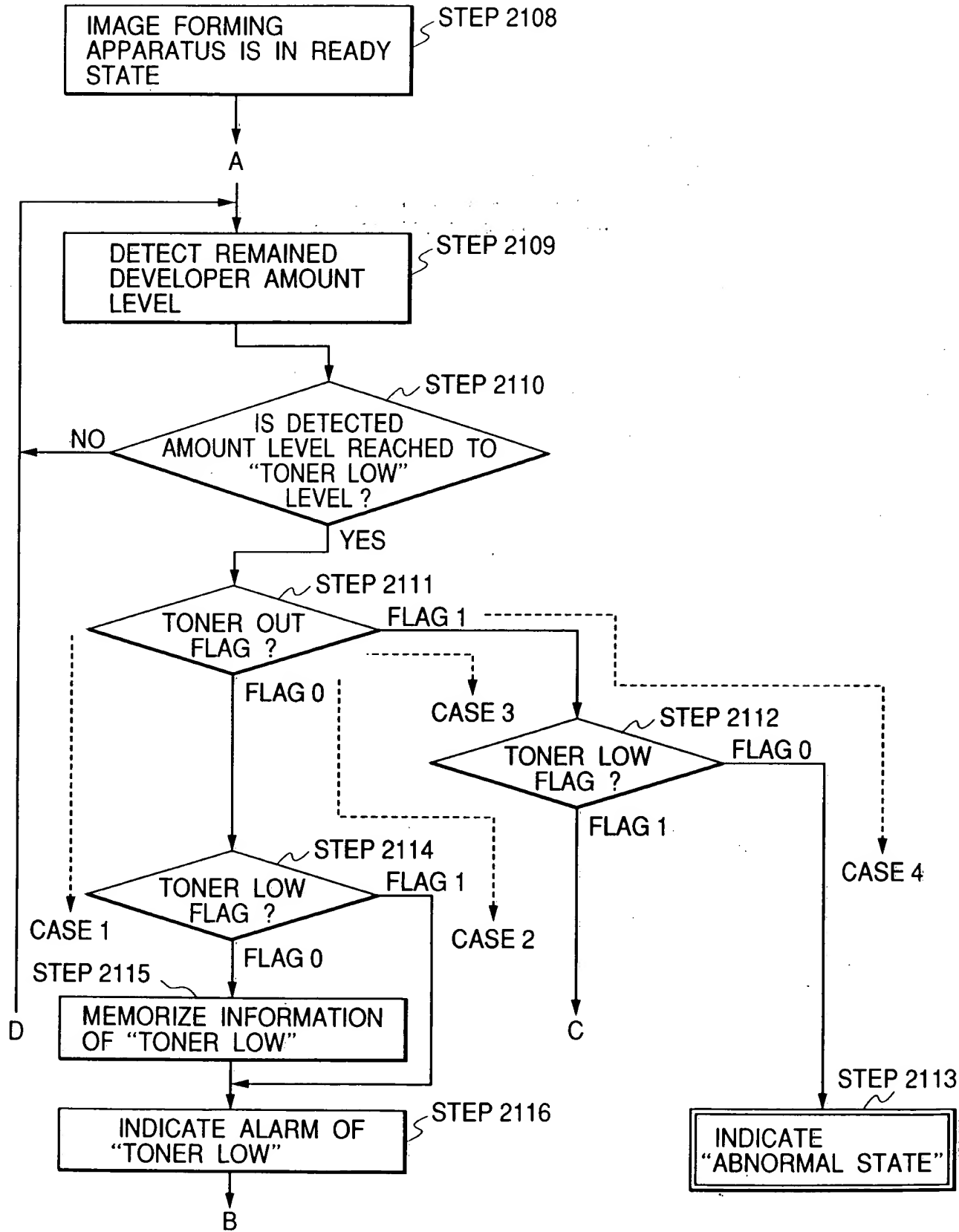
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FIG. 10



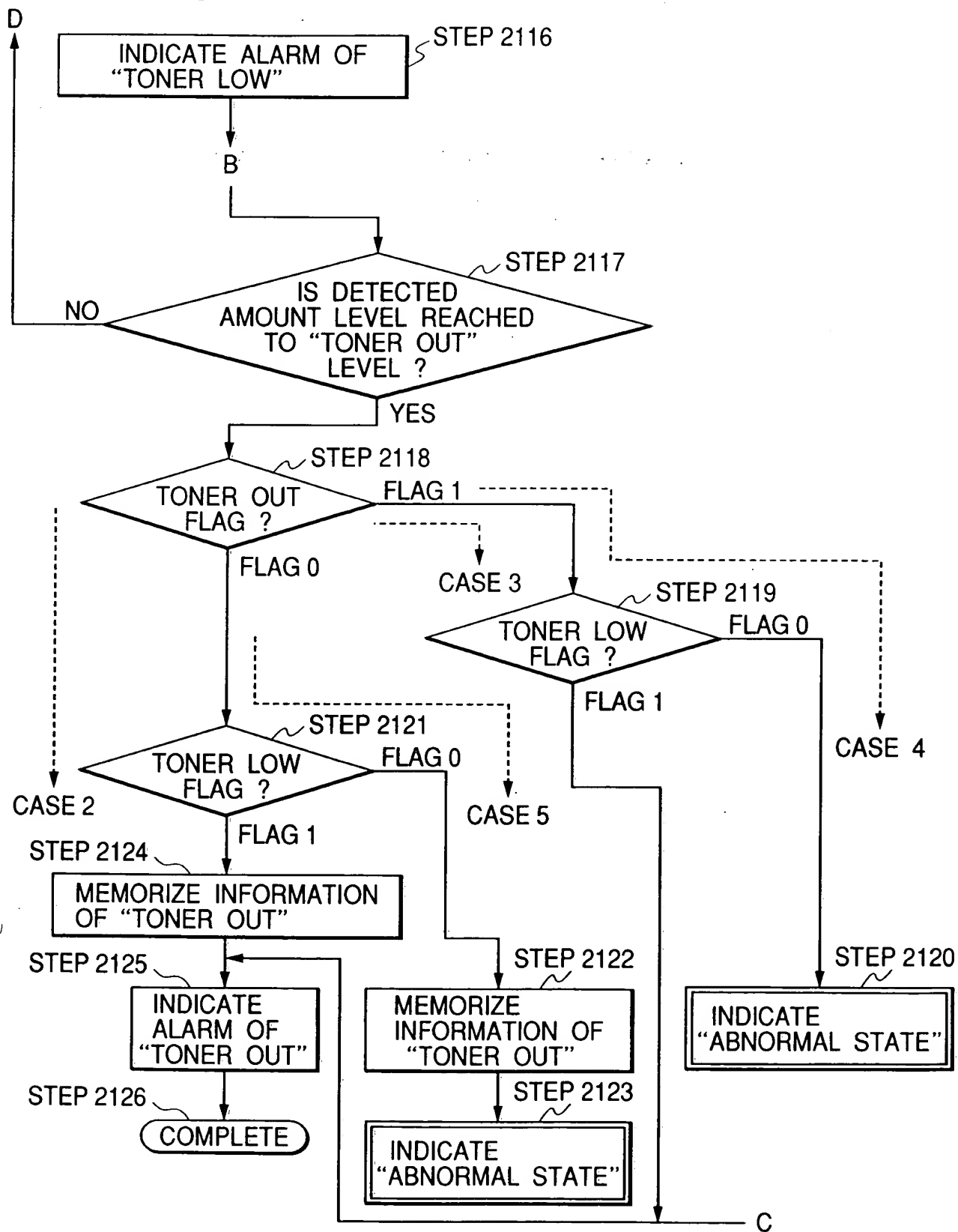
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FIG. 11



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FIG. 12



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APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 13

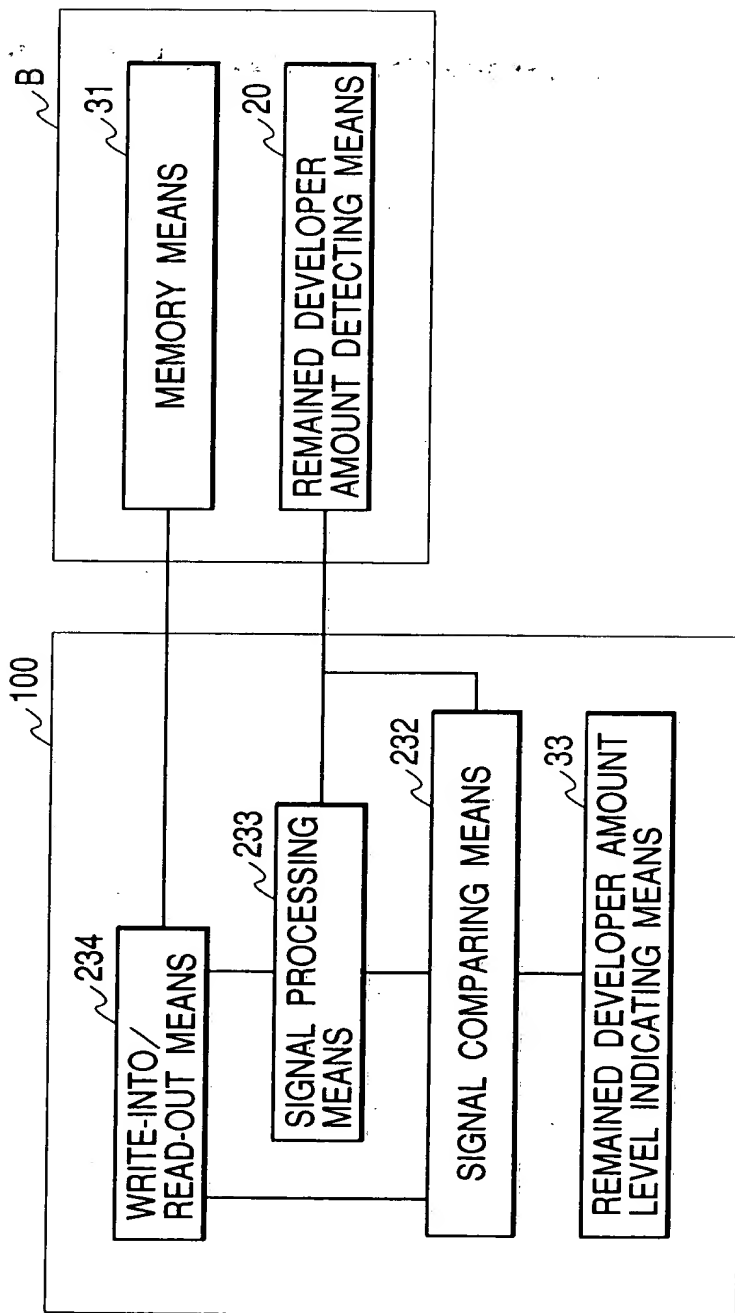
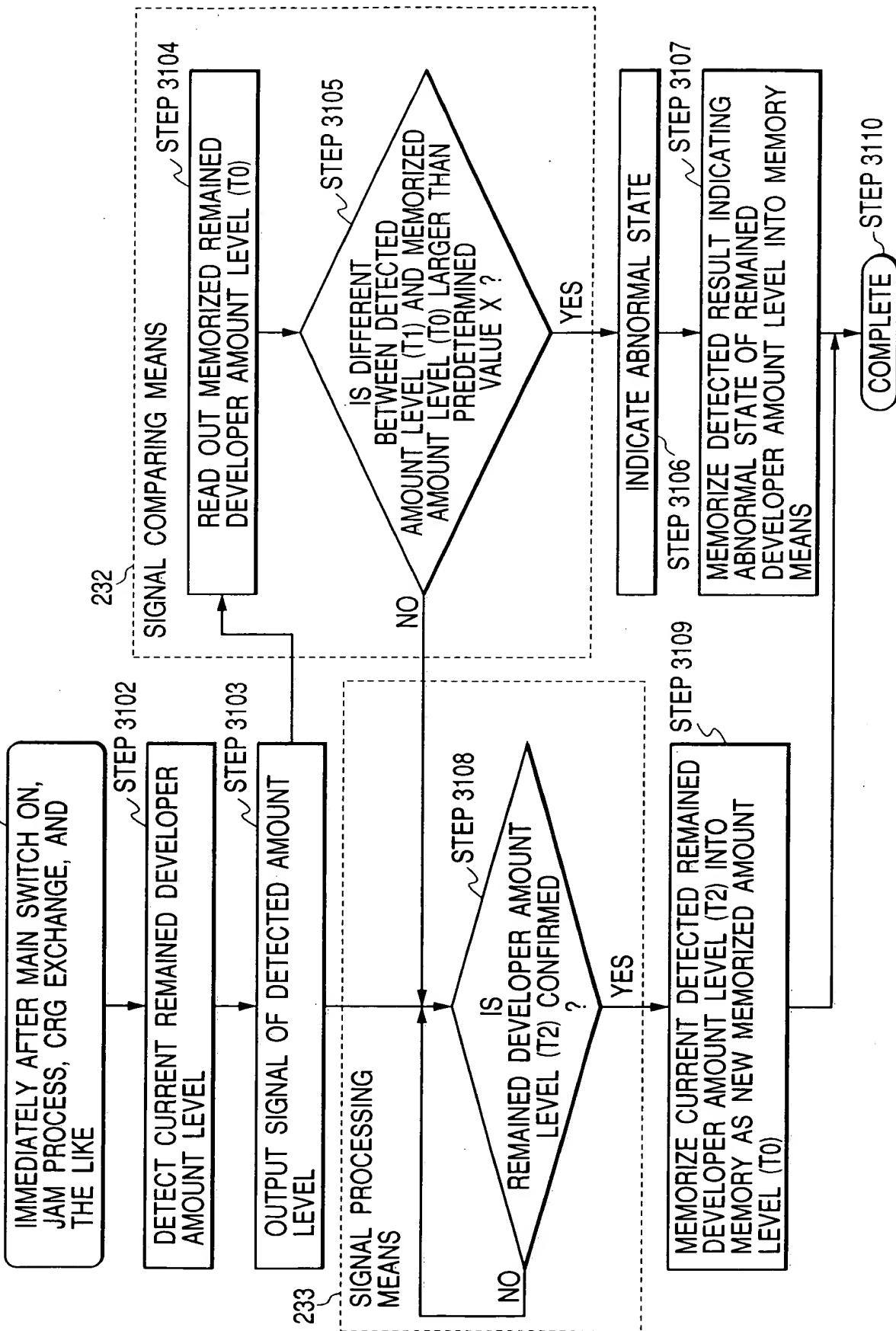
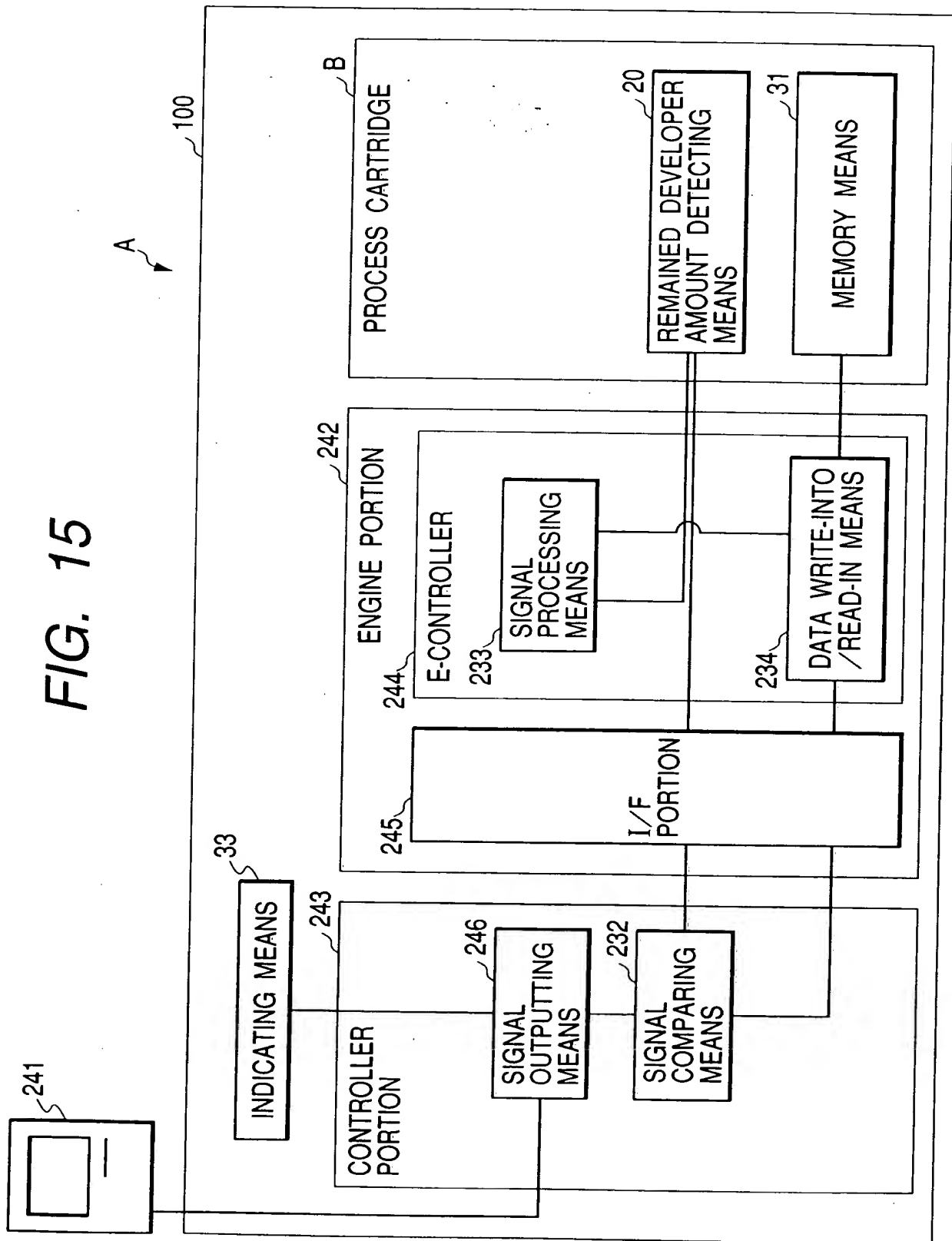


FIG. 14



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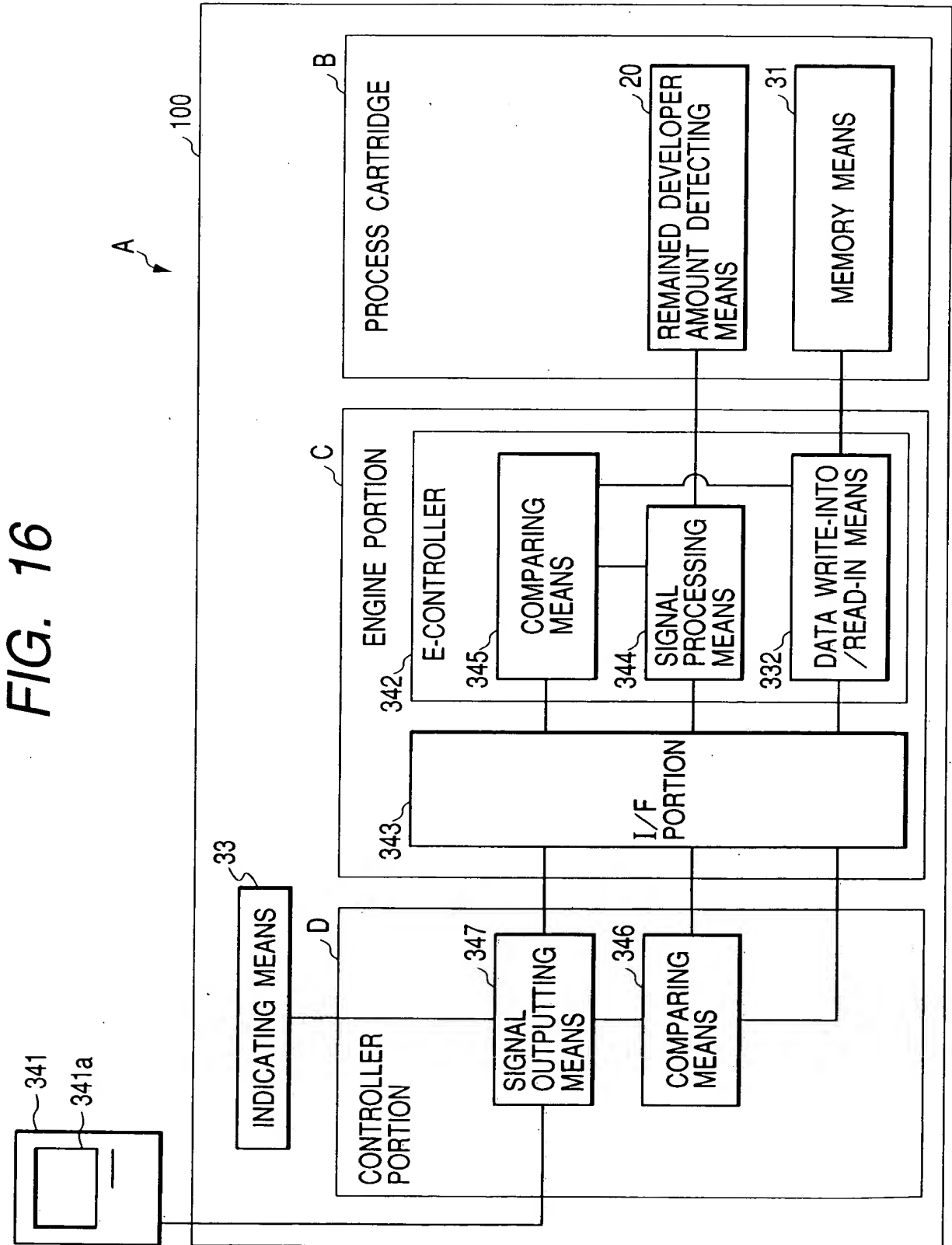


FIG. 17

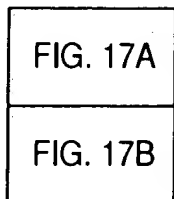
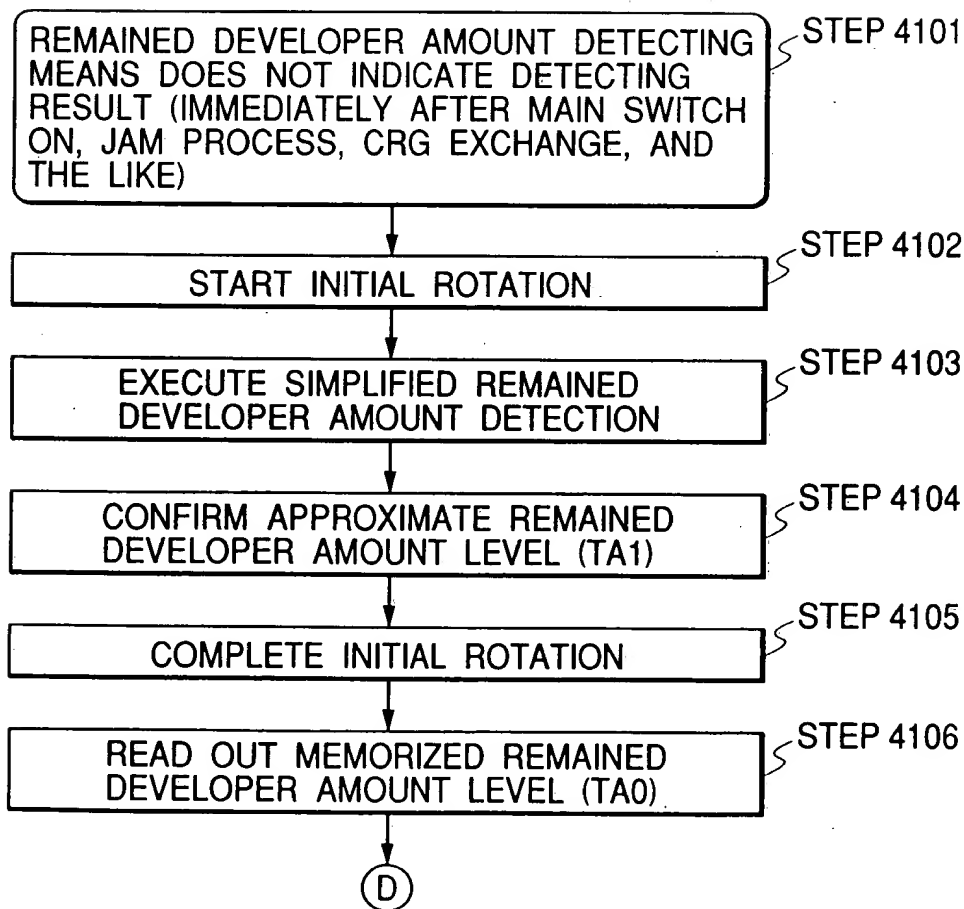
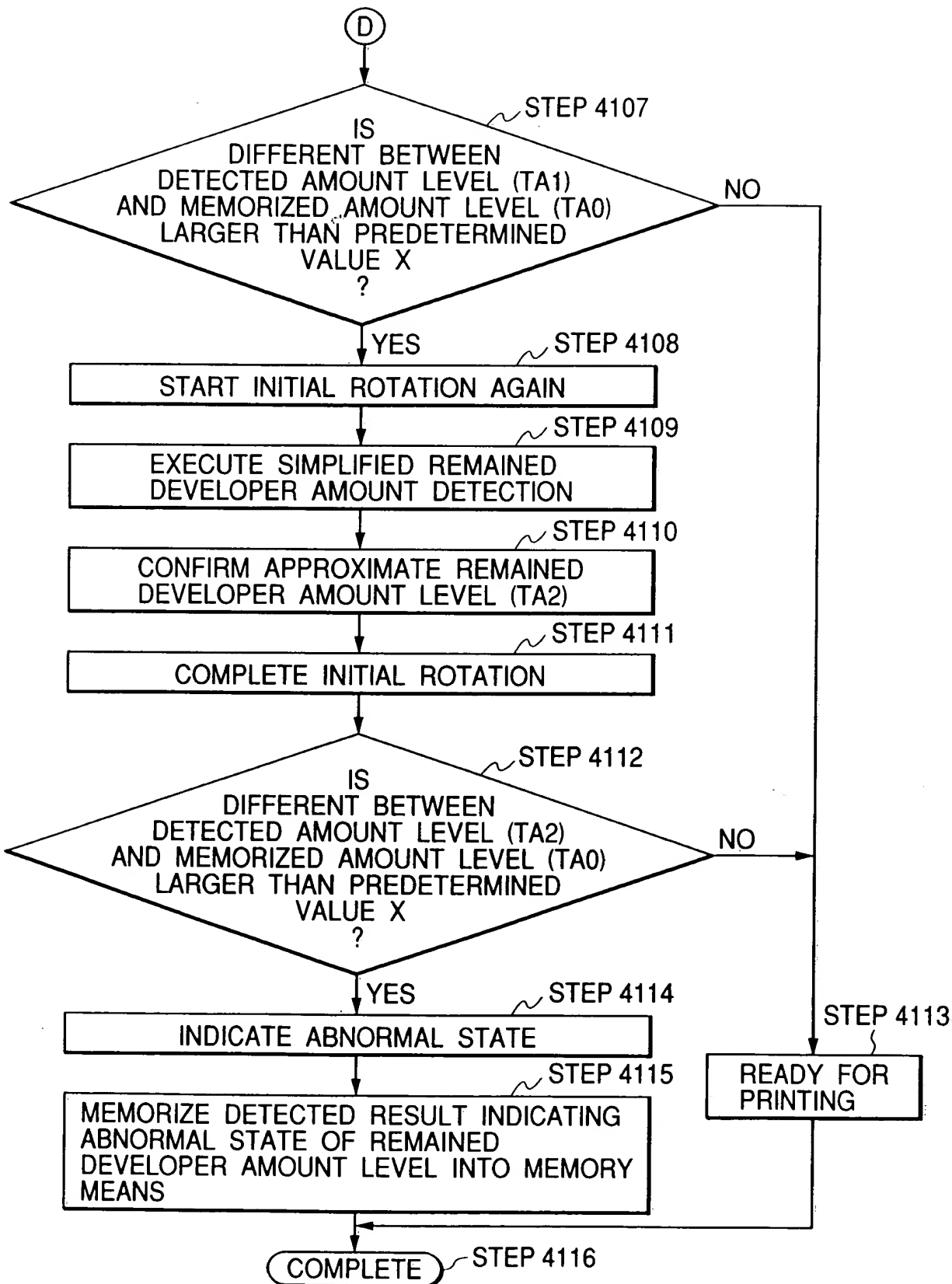


FIG. 17A



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FIG. 17B



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FIG. 18

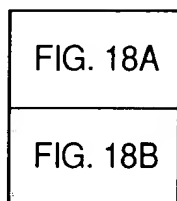
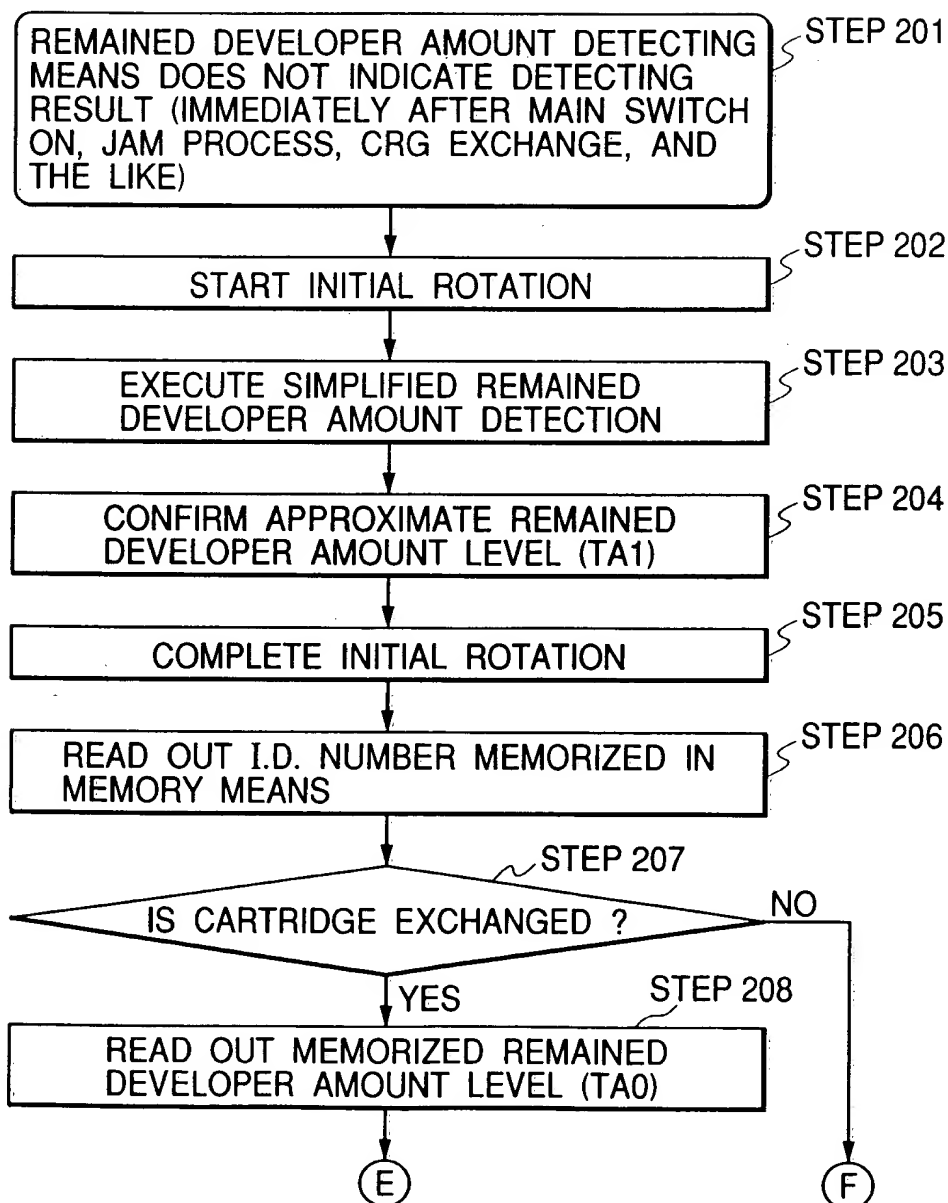


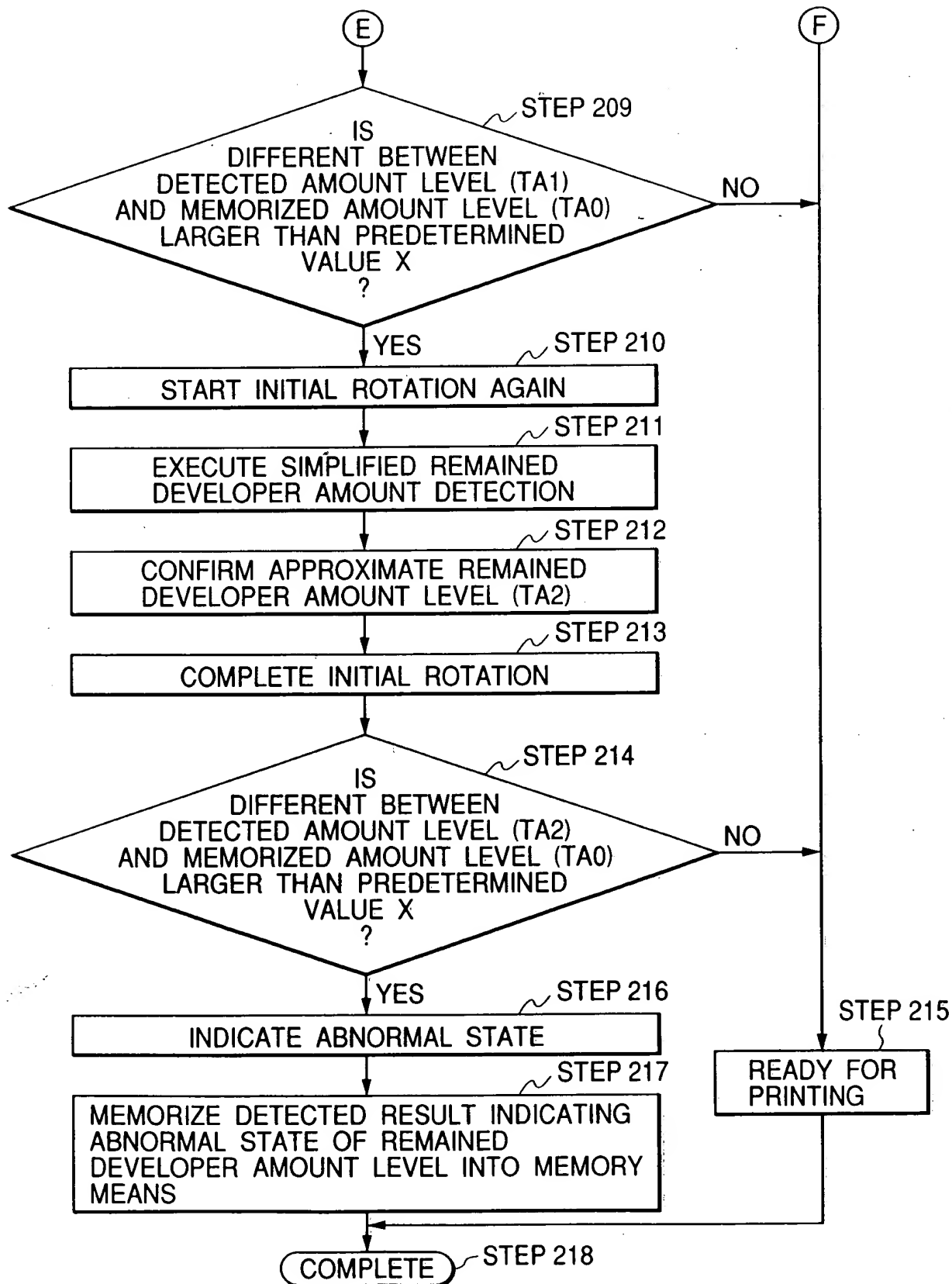
FIG. 18A



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FIG. 18B



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FIG. 19

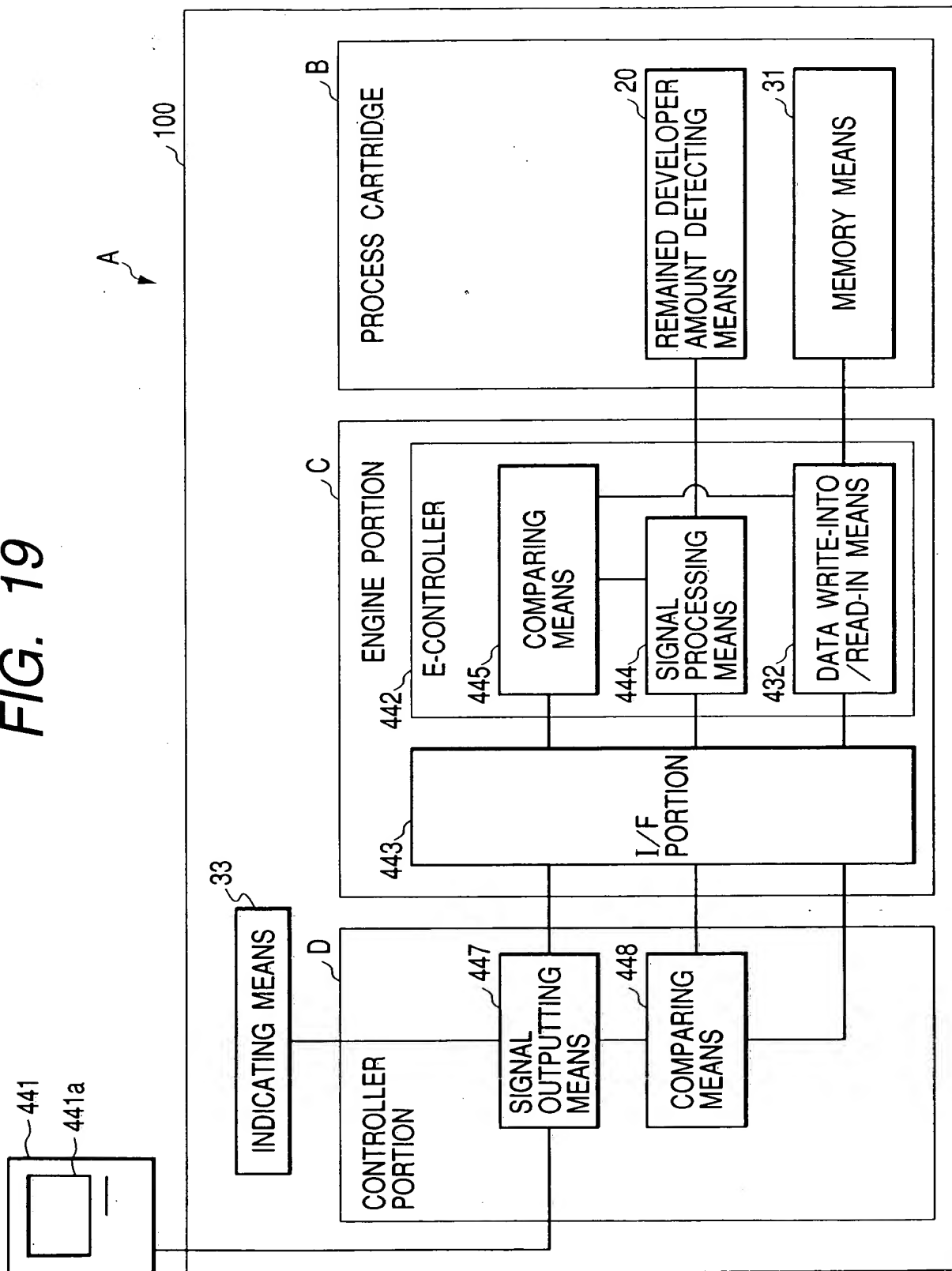


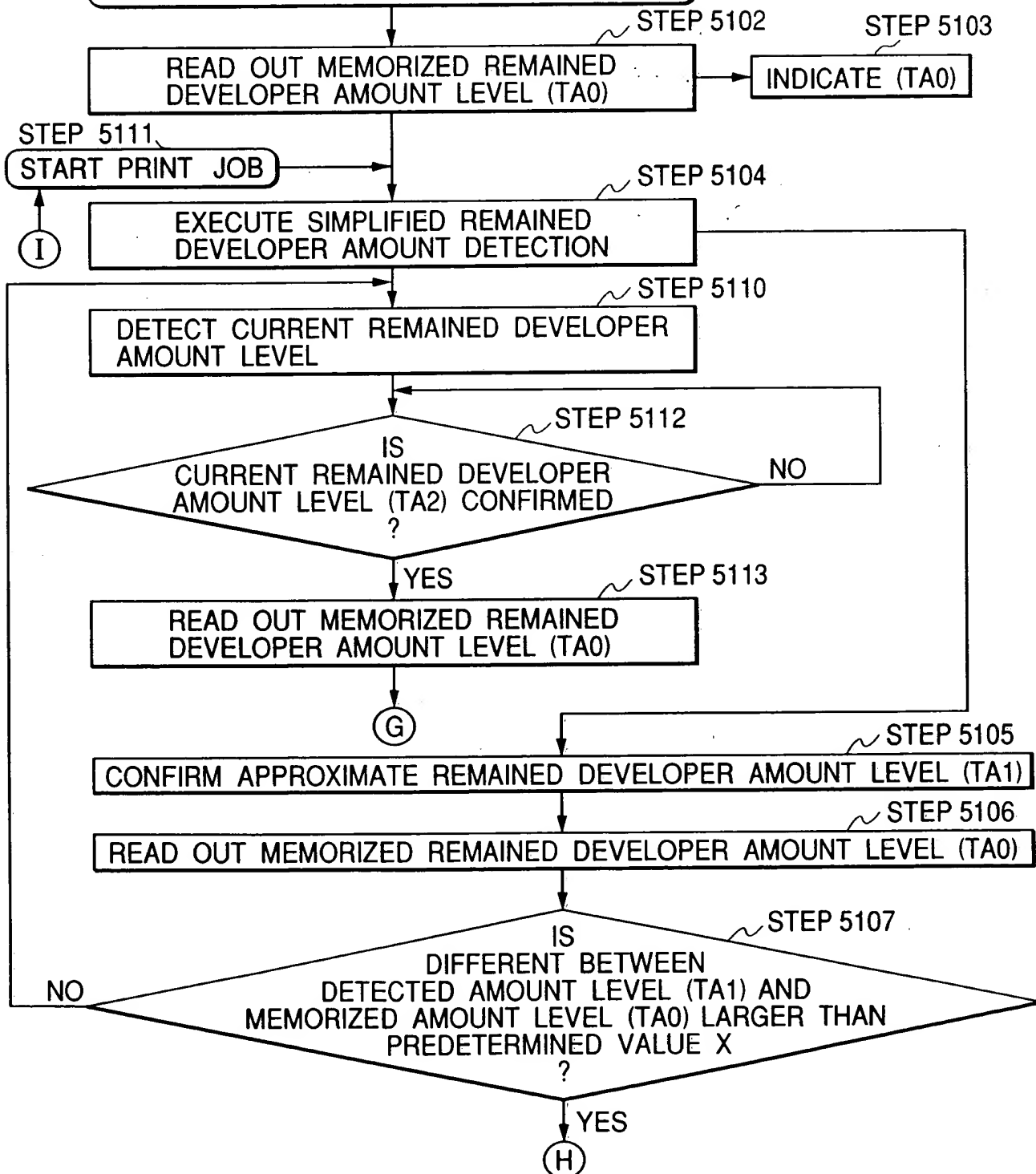
FIG. 20A STEP 5101

FIG. 20

REMAINED DEVELOPER AMOUNT DETECTING MEANS DOES NOT INDICATE DETECTING RESULT (IMMEDIATELY AFTER MAIN SWITCH ON, JAM PROCESS, CRG EXCHANGE, AND THE LIKE)

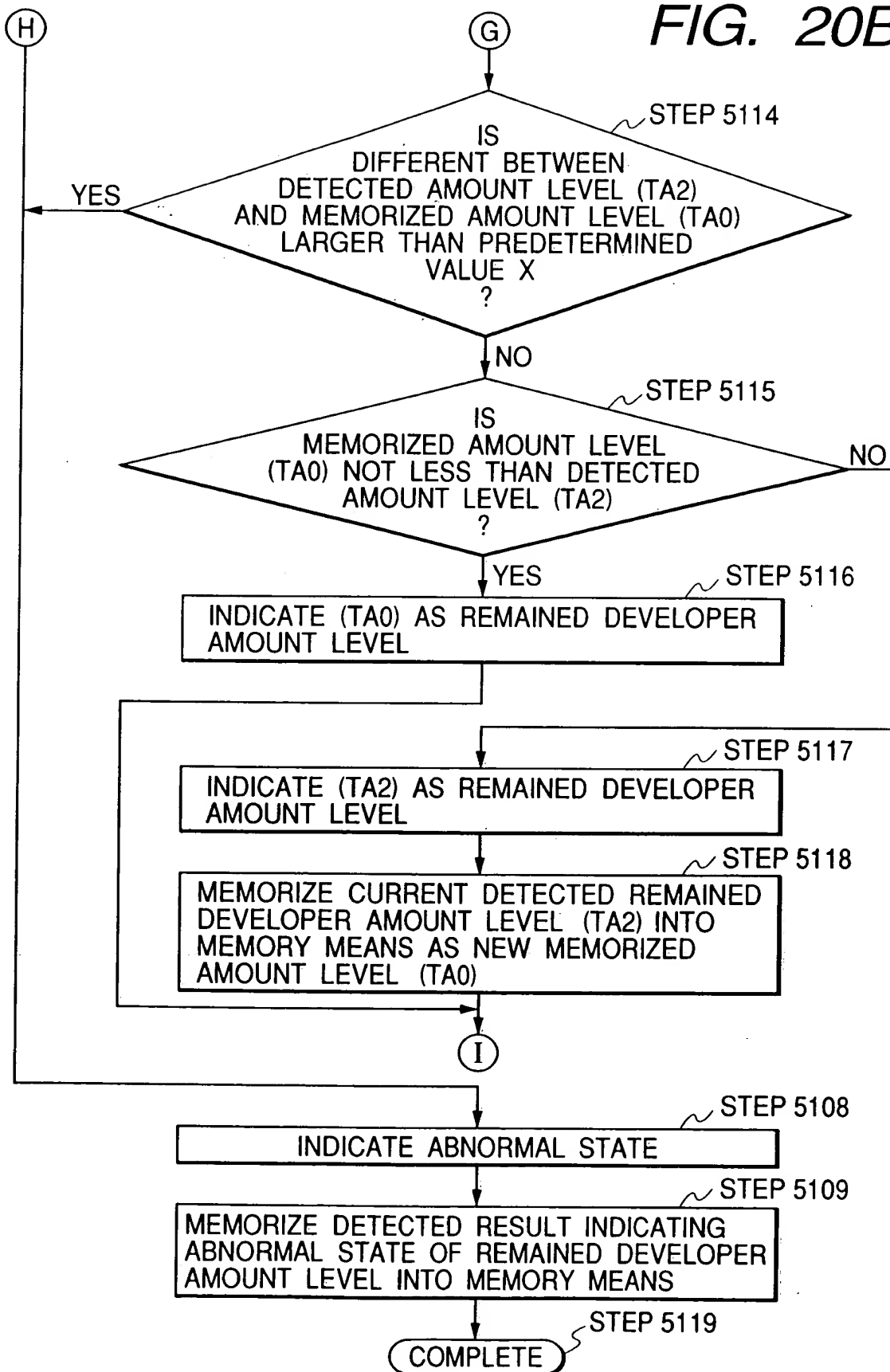
FIG. 20A

FIG. 20B



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FIG. 20B



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